



Attorney Docket SEL 198

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of
Yamazaki et al.
Serial No.: 09/610,753
Filed: July 6, 2000
For: Semiconductor Device And
Manufacturing Method Thereof
Art Unit: 2815
Examiner: P. Brock III

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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the Assistant Commissioner for Patents ,
P.O. Box 1450, Alexandria, VA 22313-1450 on
June 9, 2003
(Date of Deposit)
Shannon Wallace
Name of applicant, assignee, or Registered Rep.
Shannon Wallace June 9, 2003
Signature Date

RESPONSE D

Sir:

Applicants have the following response to the Office Action of March 7, 2003. Applicants will address each of the Examiner's objections and rejections in the order in which they appear in the Office Action.

I. Drawings

The Examiner is now objecting to the drawings under 37 CFR §1.83(a) as not showing every feature of the invention specified in the claims. In particular, the Examiner states that "the p-channel TFT of the driver circuit not having an LDD region must be shown or the feature canceled from the claim(s)."

Applicants disagree with this statement by the Examiner. For example, page 29, lns. 12 - 15 and page 29, ln. 24 - page 30, ln. 3 of the present application state that p-channel TFT 200, source

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